

Gschwind et al
1089-2001-0000 (RPT) (8728-545)

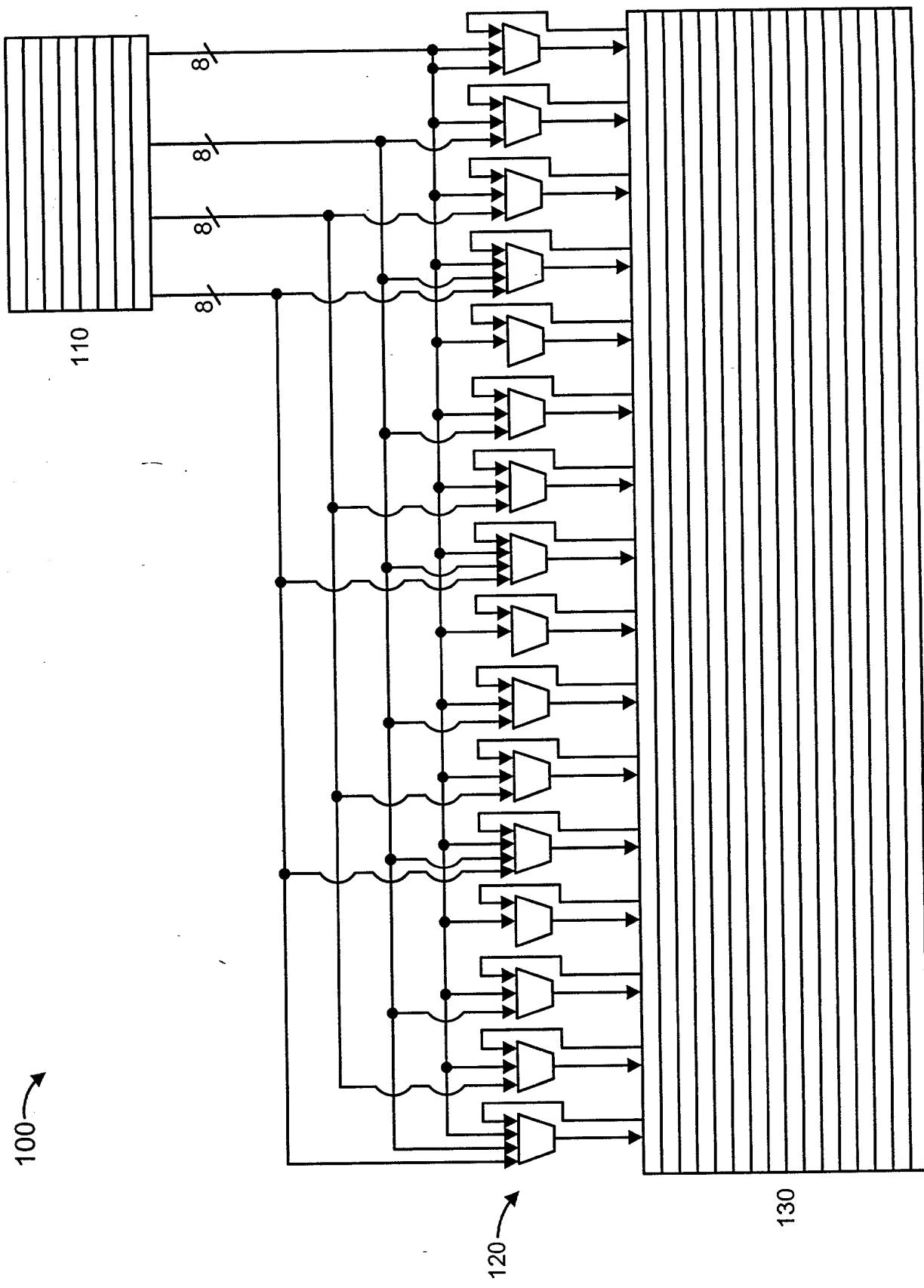


FIG. 1
(PRIOR ART)

218
109-2001-DW06 (8M28-545)

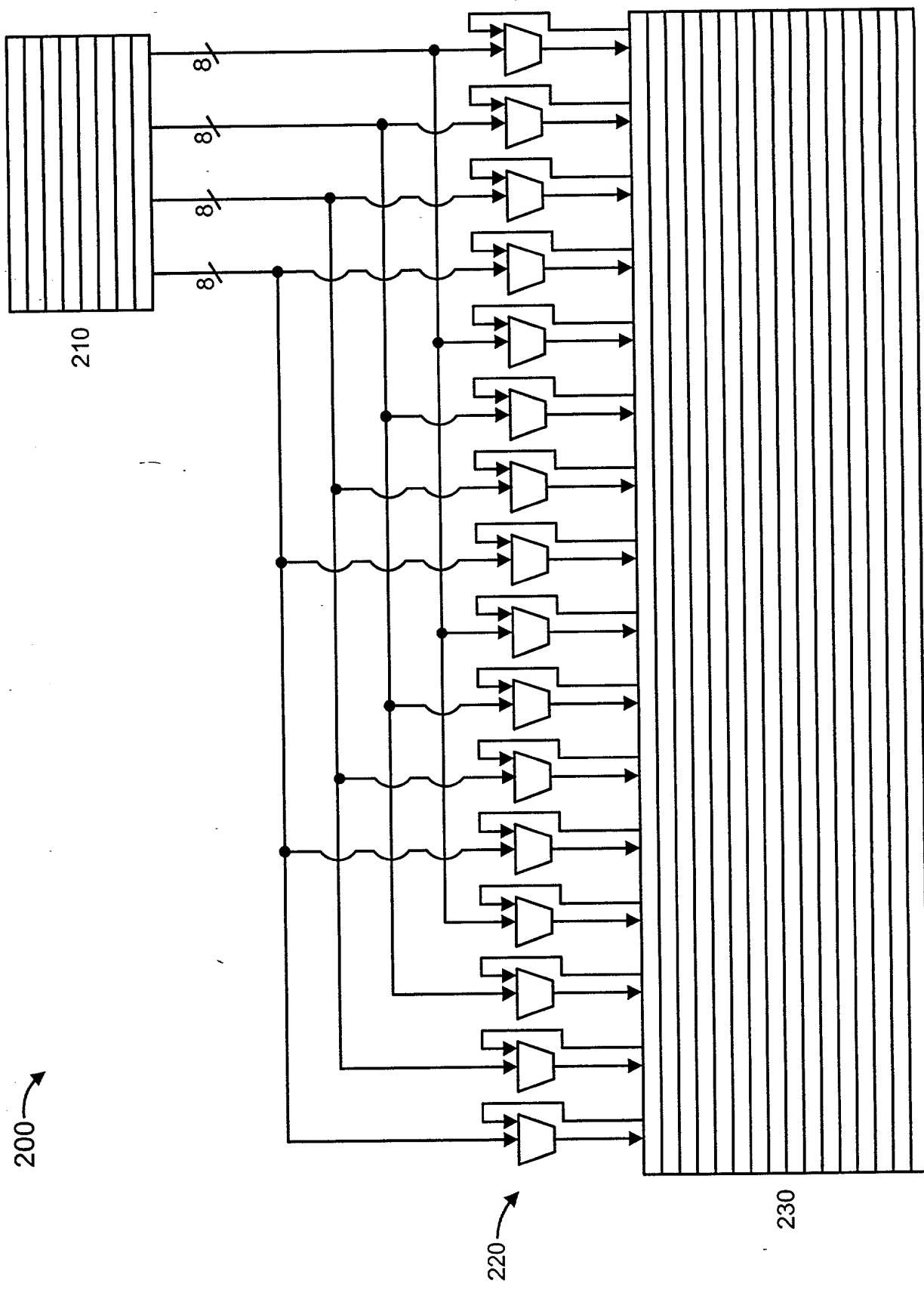


FIG. 2
(PRIOR ART)

3 / 8
YDR9-2001-0600 (8728-545)

300

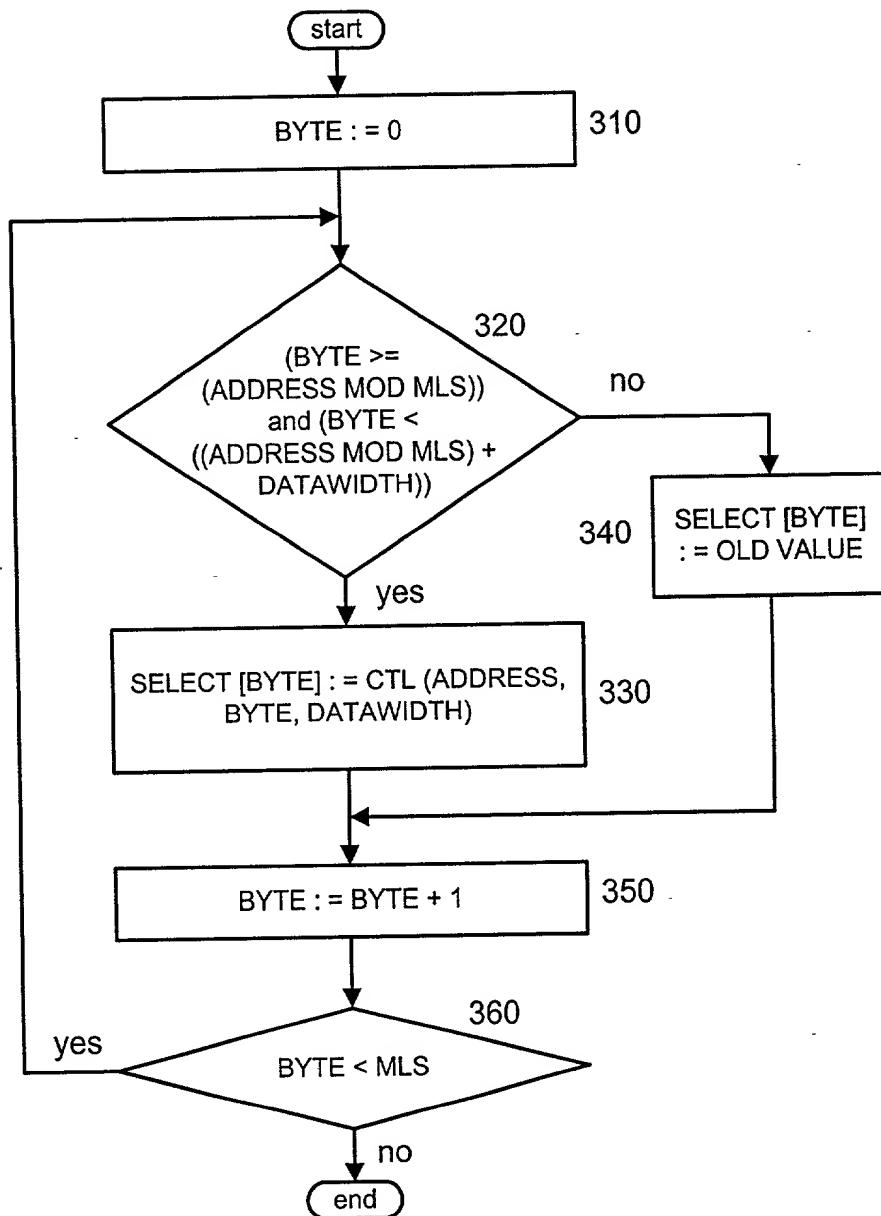


FIG. 3
(PRIOR ART)

4 / 8
YDK9-2001-DL606 (8M28-545)

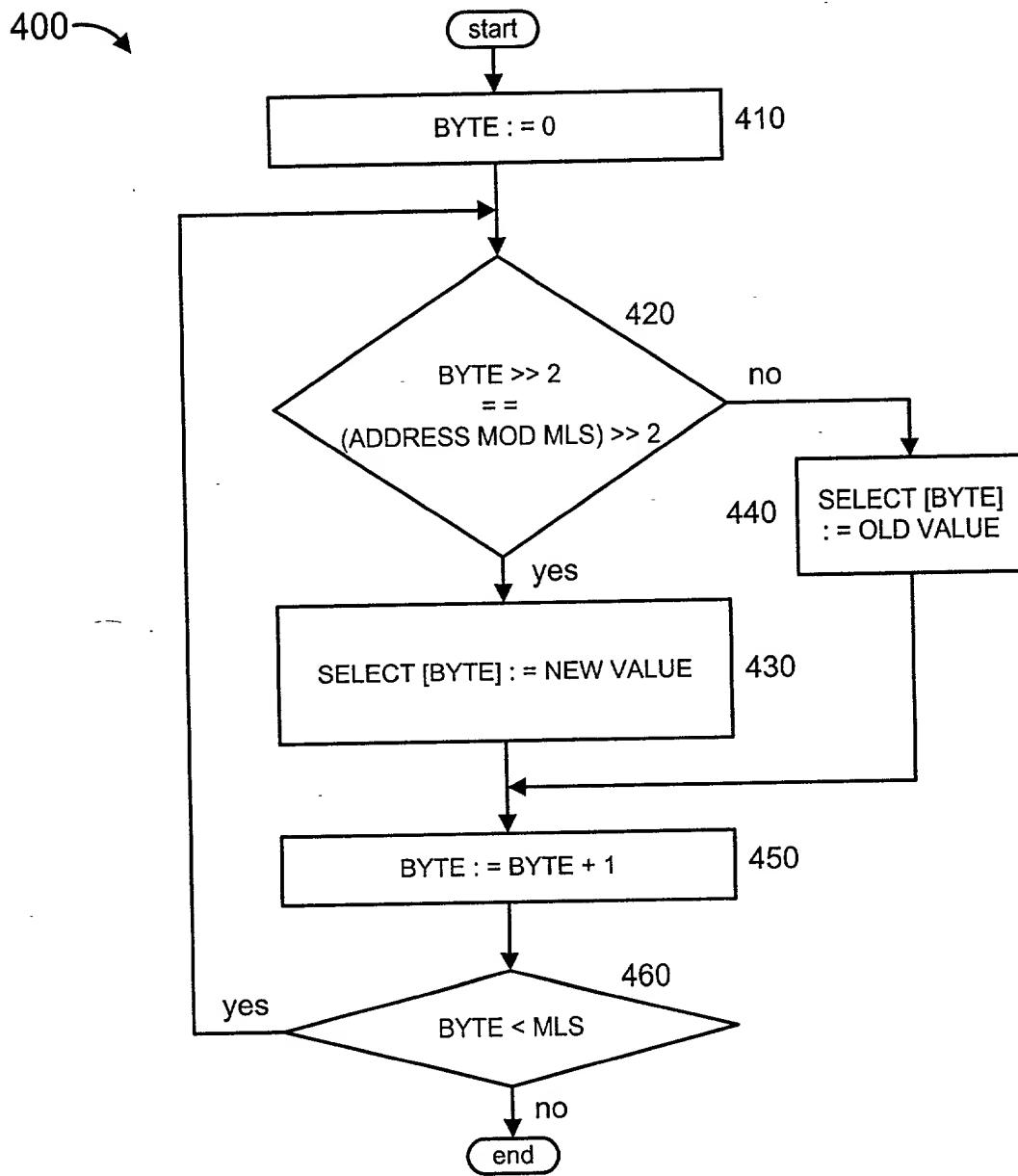


FIG. 4
(PRIOR ART)

5 / 8
YDR9-2001-D00D6 (8728-545)

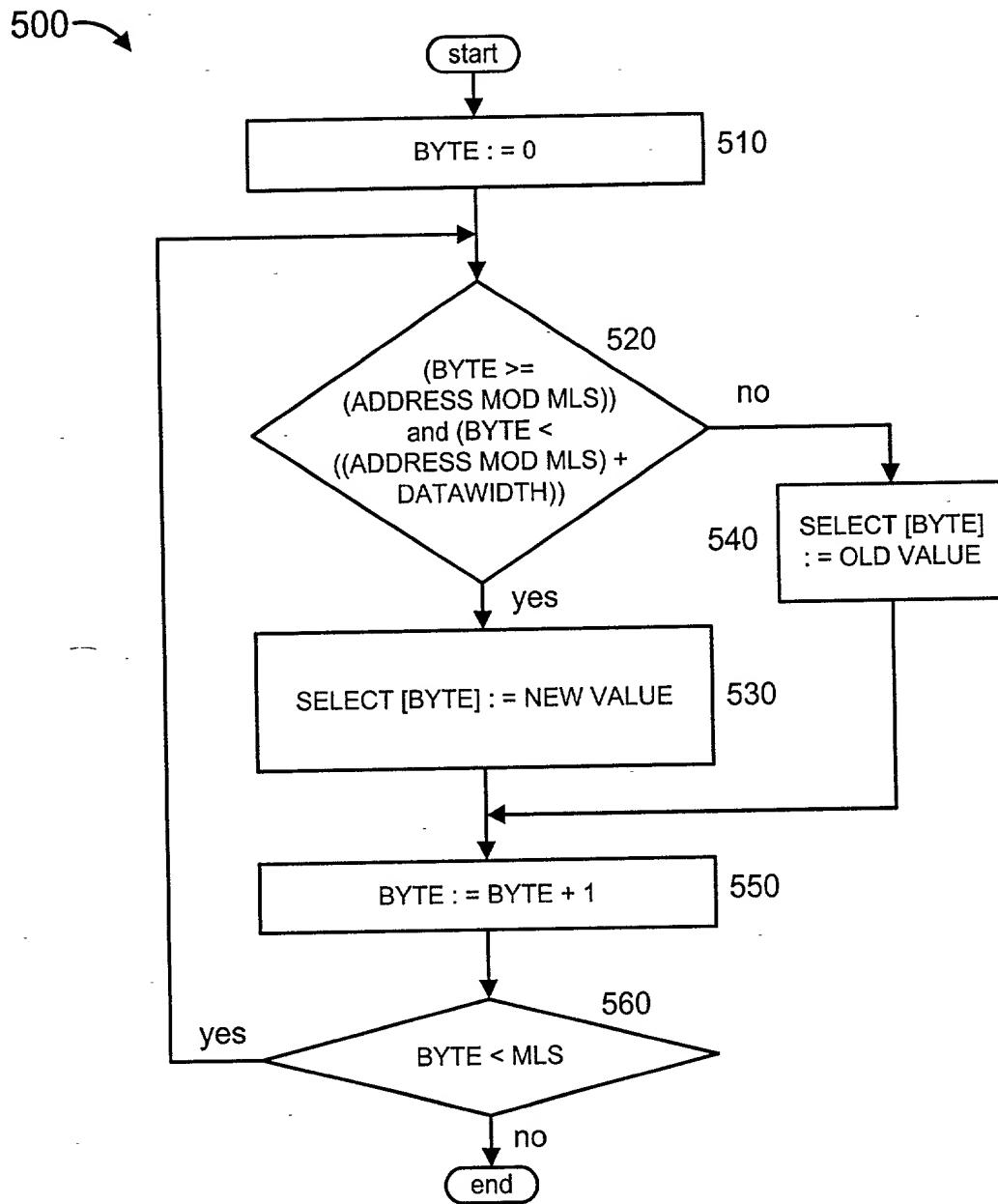


FIG. 5

1 DR9-2001-D606 (8728-545)

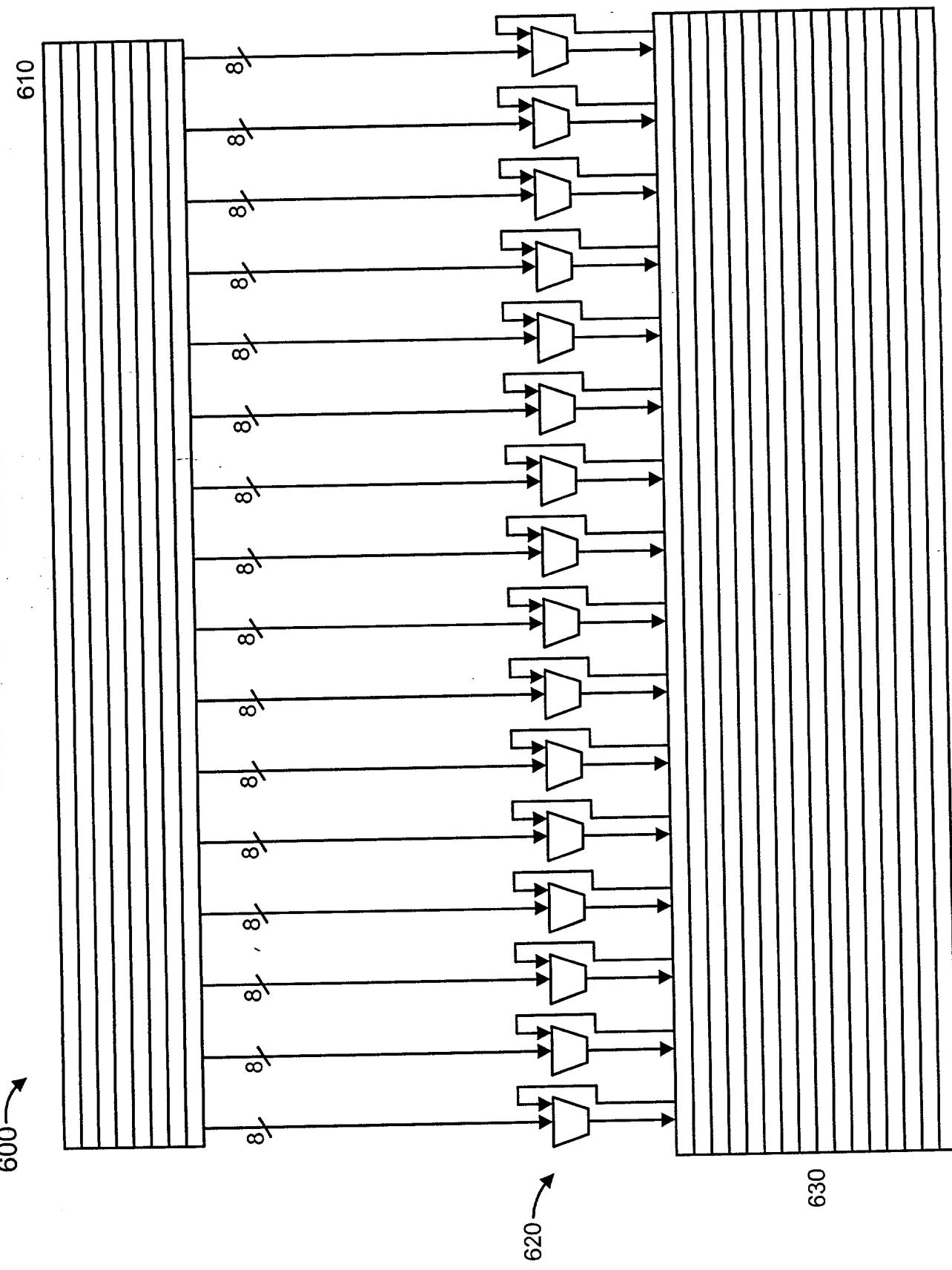


FIG. 6

7 / 8
YDK9-2001-0606 (8728-545)

subfi r3, r5, 3 ;	compute rotate count	$r3 = 3 - r5$
rotbl r2, r4, r3	; rotate bytes left	$r2 = r4 \text{ ROTL } (r3 * 8)$
stb r2, 0(r5)	; store byte	mem[r5] = r2 using a 1-byte mask aligned with address 0(r5)

FIG. 7

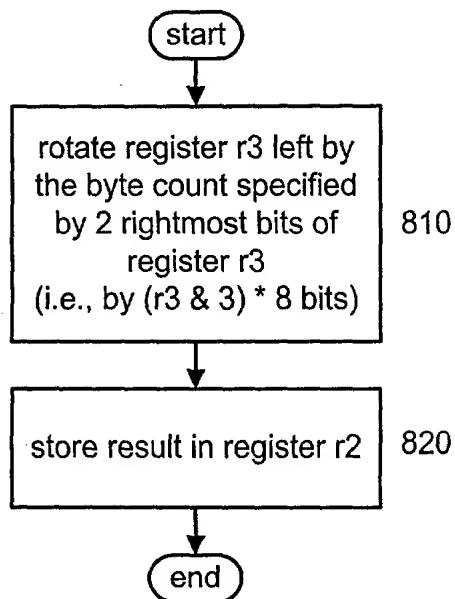


FIG. 8
(PRIOR ART)

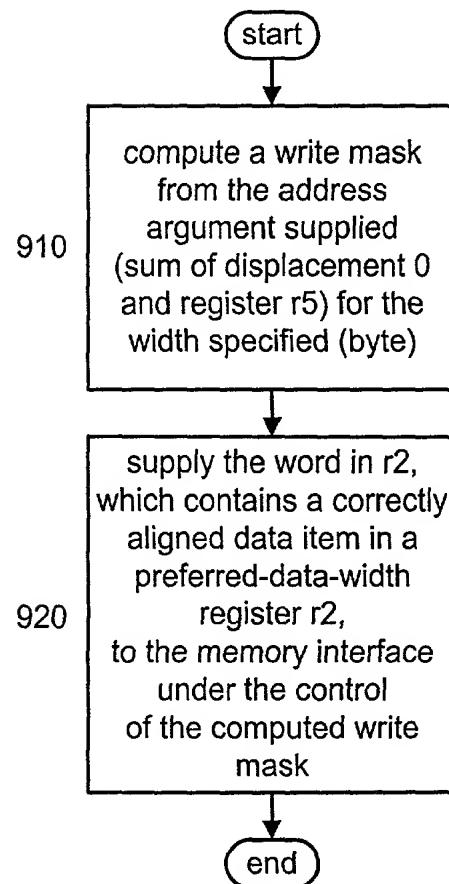


FIG. 9

8 | 8
109-2001-DL0D0 (828-545)

subfi r3, r5, 12	; compute rotate count	$r3 = 12 - r5$
vrotbl r2, r4, r3	; rotate bytes left	$r2 = r4 \text{ ROTL } (r3 * 8)$
vstw r2, 0(r5)	; store word	mem[r5] = r2 using a 1-word (4-byte) mask aligned with address 0(r5)

FIG. 10

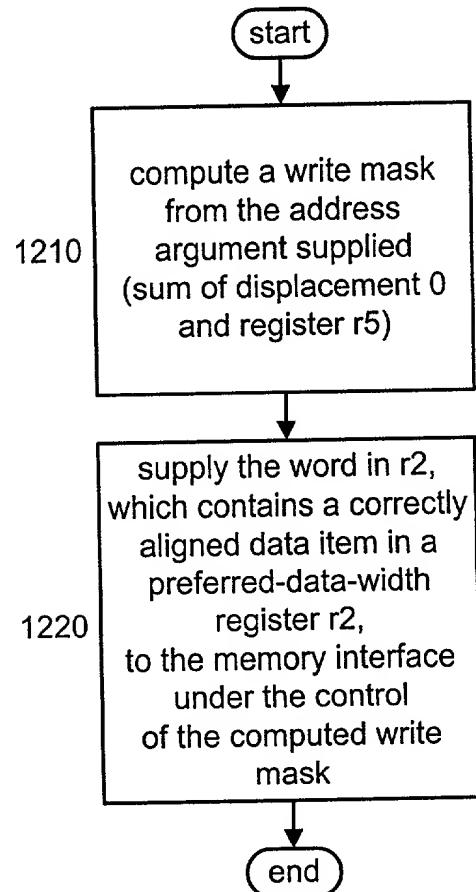
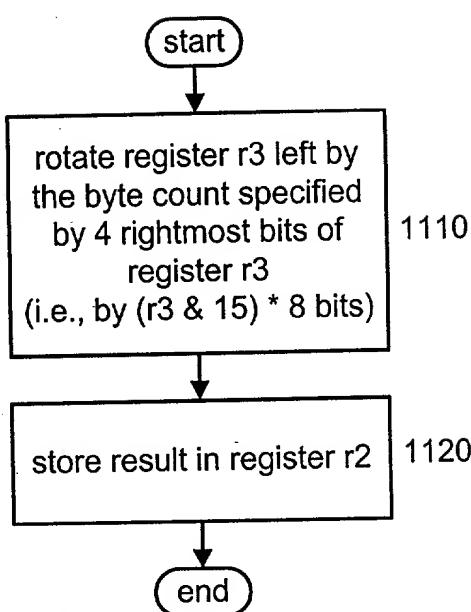


FIG. 11
(PRIOR ART)

FIG. 12